

MULTIPLE CHIP STACK STRUCTURE AND COOLING SYSTEM

Farrar et al.

Appl. No.: Unknown

Atty Docket: MICRON.241DV1

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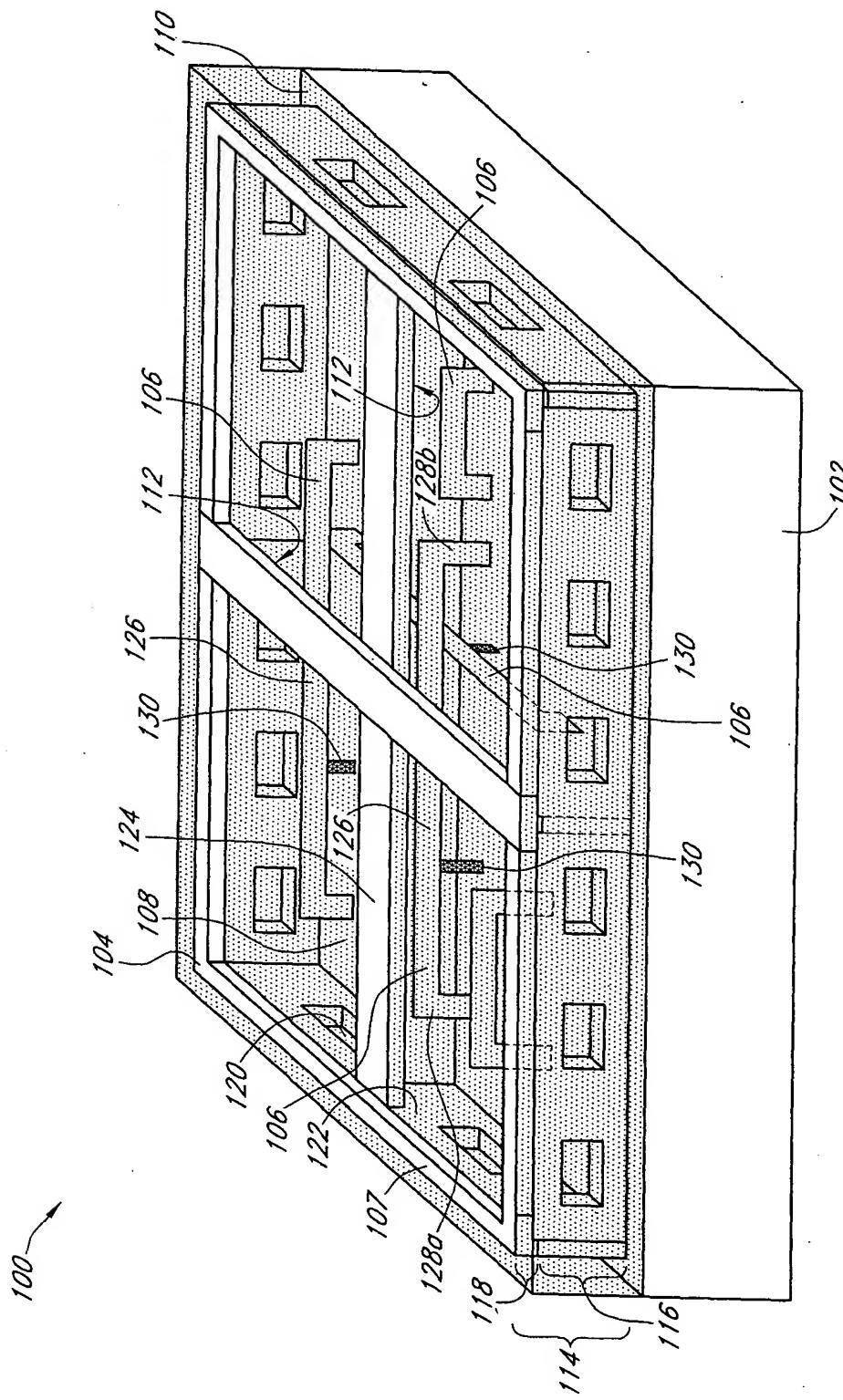


FIG. 1

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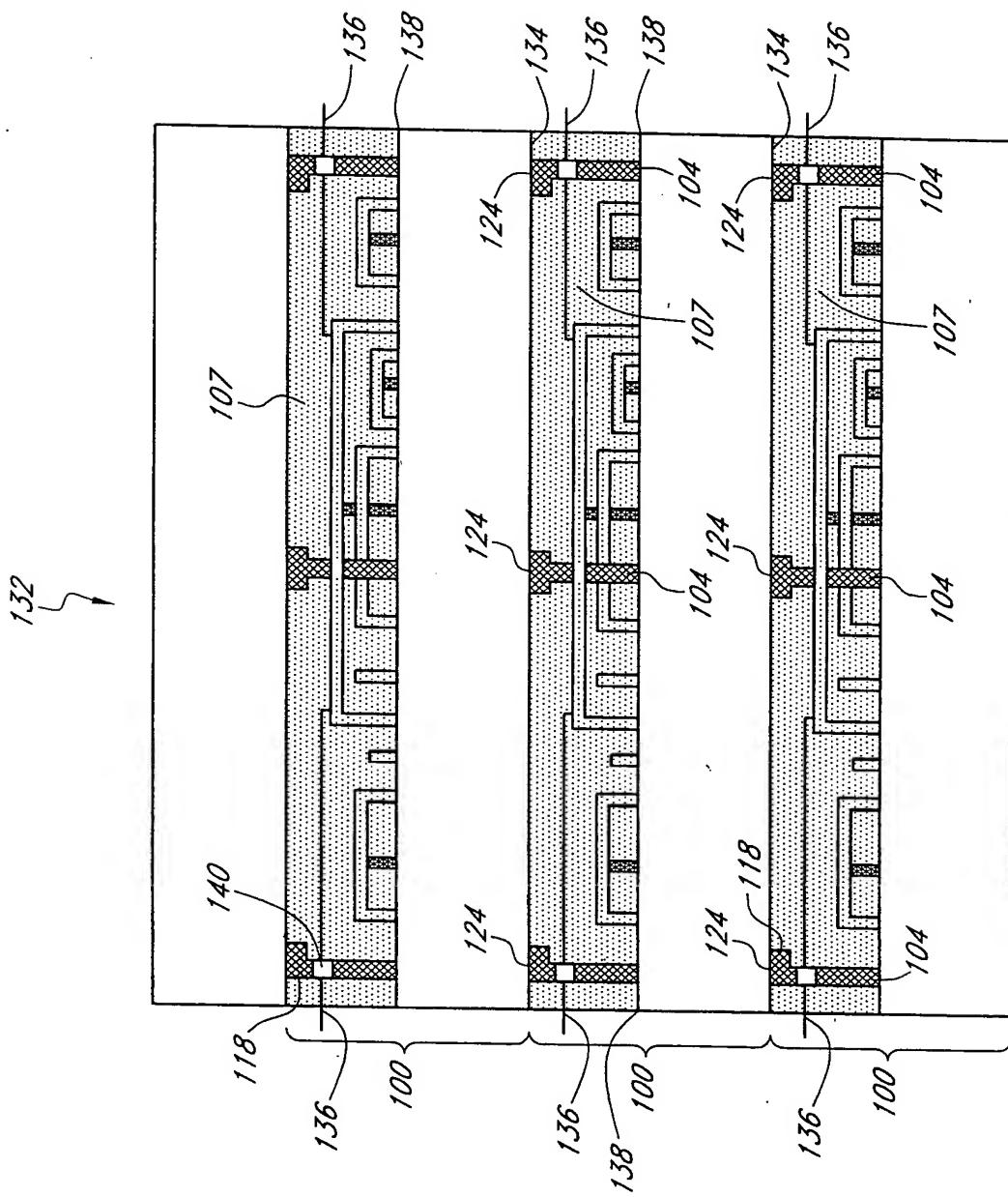


FIG. 2

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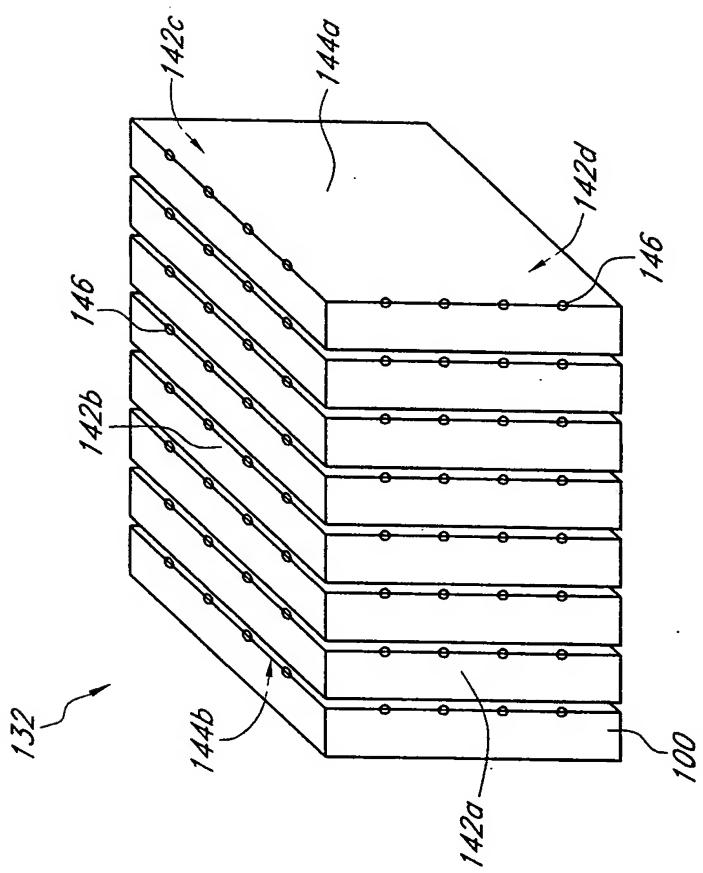


FIG. 3

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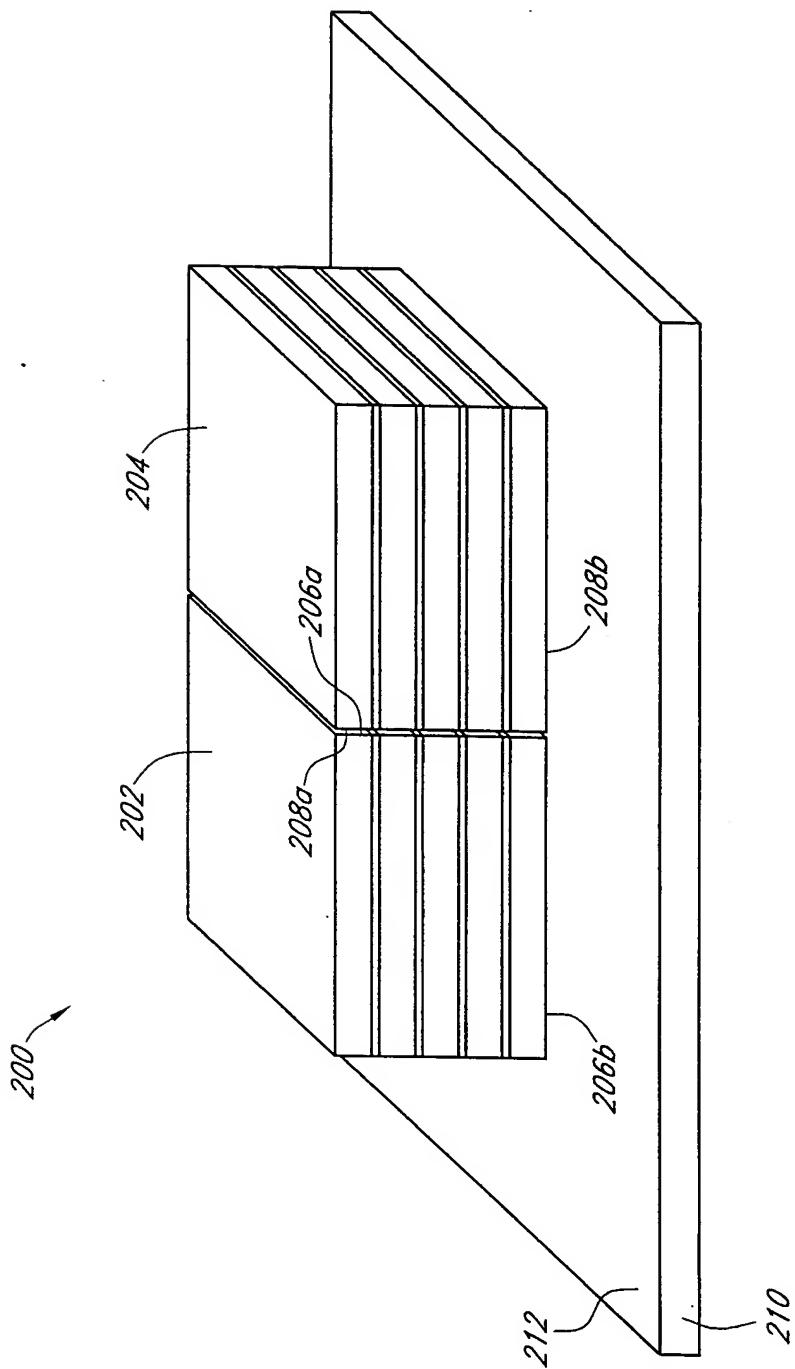


FIG. 4

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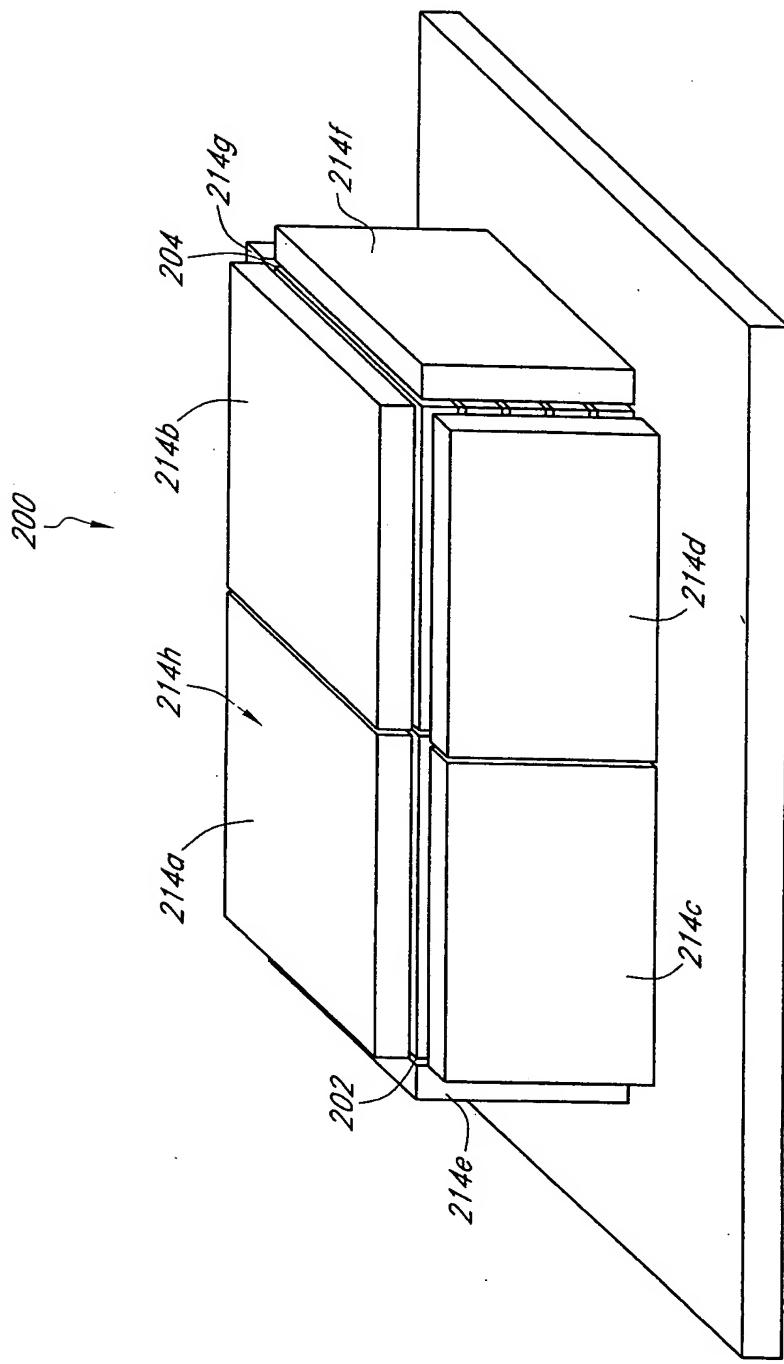


FIG. 5

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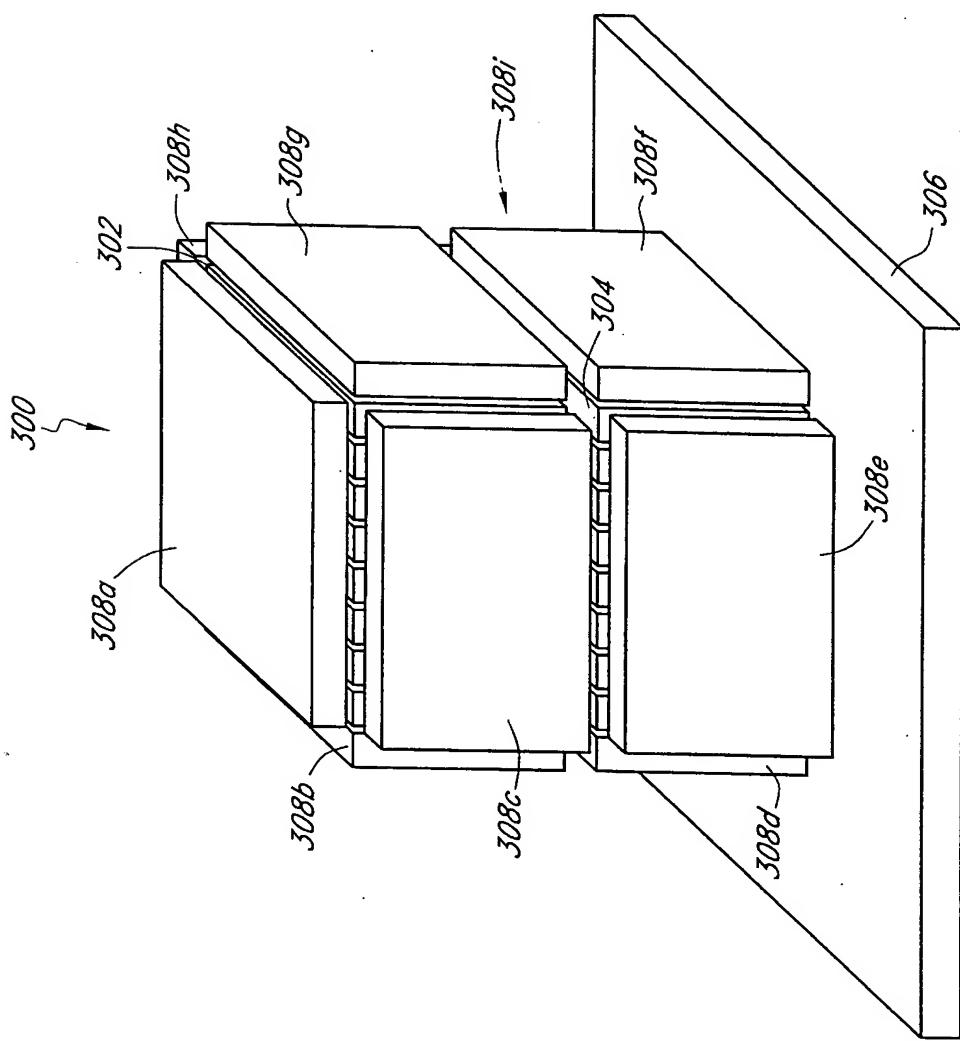


FIG. 6

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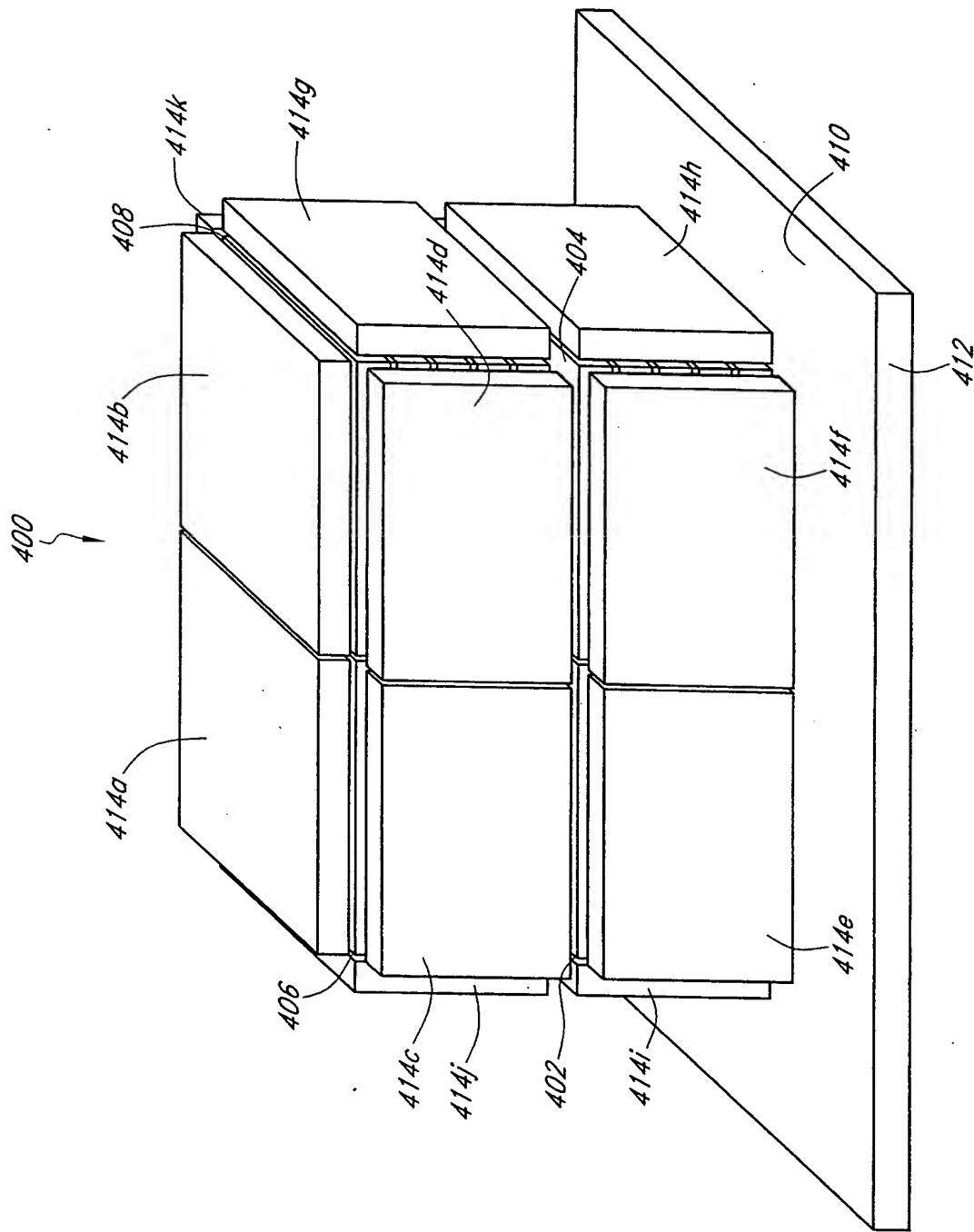


FIG. 7

MULTIPLE CHIP STACK STRUCTURE AND COOLING SYSTEM

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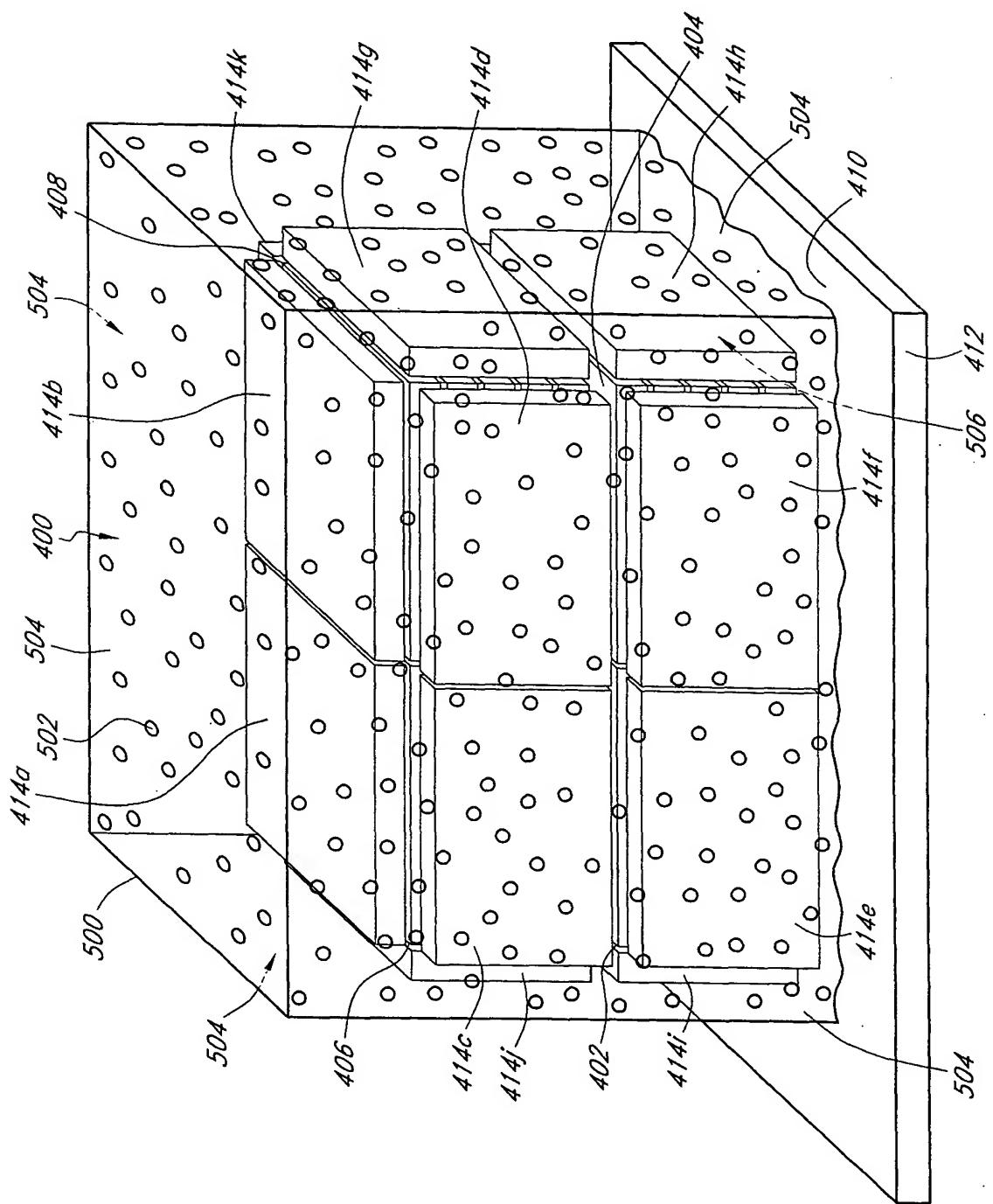


FIG. 8